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Renne M. F. Wyzykowski

Applicant : Chuong Vu
Application No. : 10/020,491
Filed : December 11, 2001
Title : METHOD OF AUTOMATIC LOAD BALANCING IN
SWITCH FABRICS
Grp./Div. : Unknown
Examiner : To Be Assigned
Docket No. : 45313/DMC/V165

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INFORMATION DISCLOSURE STATEMENT
37 CFR § 1.97(b)

Assistant Commissioner for Patents
Washington, D.C. 20231

Post Office Box 7068
Pasadena, CA 91109-7068
February 5, 2002

Commissioner:

In compliance with the duty of disclosure under 37 CFR §§ 1.56, 1.97 and 1.98, and in accordance with the provisions in the Manual of Patent Examining Procedure §§ 609 and 707.05(b), enclosed is FORM PTO-1449 listing the references that are known to applicant. Copies of each of the listed references are enclosed. This filing is timely because it is made during one of the periods described in 37 CFR § 1.97(b).

It is respectfully requested that the listed references be considered in the examination of this application and identified on the list of references cited on the patent issuing for this application. Applicant also requests that an initialed copy of FORM PTO-1449 be entered in the application file and returned to applicant with the next communication from the Office in accordance with MPEP § 609.

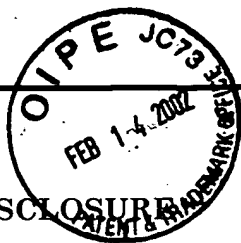
Respectfully submitted,
CHRISTIE, PARKER & HALE, LLP

By
Patrick Y. Ikehara, Reg. No. 42,681
626/795-9900

INFORMATION DISCLOSURE

STATEMENT BY APPLICANT

(use as many sheets as necessary)



Attorney Docket Number	45313/PYI/V165
Application Number	10/020,491
Filing Date	December 11, 2001
Applicant(s)	Chuong Vu
Group Art Unit	Unknown
Examiner Name	To Be Assigned

U.S. PATENT DOCUMENTS

EXAMINER INITIALS	DOCUMENT NUMBER	ISSUE DATE	PATENTEE	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE

FOREIGN PATENT DOCUMENTS

EXAMINER INITIALS	DOCUMENT NUMBER	PUBLICATION DATE	COUNTRY OR PATENT OFFICE	CLASS	SUBCLASS	TRANSLATION	
						YES	NO

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OTHER DOCUMENTS

EXAMINER INITIALS	Include name of the author (in CAPITAL LETTERS), title of the article, title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.
	Wikander, "A Multi-Chip Architecture for High-Capacity Packet Switching", Master Thesis, Luleå University of Technology, December 30, 1999 (pp 1-61)
	Buhrgard, et al., "A scaleable ATM switching solution", Ericsson Review, No. 1, 1998 (pp 24-31)
	Piekarski, "Reinventing the Switch Fabric Architecture", URL: http://www.commsdesign.com/story/OEG20010521S0113 , Communications Systems Design, June 1, 2001 (6 pgs)
	IBM Microelectronics, "The Network Processor Enabling Technology for High-Performance Networking", Network Processor, 1999, (5 pgs) http://www.chips.ibm.com

EXAMINER SIGNATURE	DATE CONSIDERED

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

Patent and Trademark Office; U.S. DEPARTMENT OF COMMERCE

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